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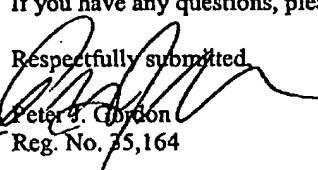
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RE: USSN 09/054,933; Attorney Docket No. A1998017

Enclosed for filing in the above application is a replacement section for Amendments to the Claims for the Reply, originally filed on 9/2/03. The previously filed Amendments to the Claims section was non-compliant. No notice to this effect was received. If you have any questions, please call the Attorney for Applicant.

Respectfully submitted,


Peter J. Gordon
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Reply Filed September 2, 2003

OFFICIAL**AMENDMENTS TO THE CLAIMS**

Please amend claims 1-4 and 9 as follows. Please cancel claims 5-8 and 10 without prejudice to the underlying subject matter. Please add claim 11 as follows.

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1. (Currently Amended) An output interface for a sender of video data having, comprising:
 an a first output for providing video data, and
 a second output for providing a valid data signal associated with the data output by the
 first output and indicating whether the associated output data includes valid video data, and
 a third output for providing a valid command signal indicating whether the data output by
 the first output includes command data, and wherein the command data includes a memory
 address at a receiver of the data,
 wherein the first, second and third outputs are in parallel; and
 wherein, in response to a request signal received from the receiver, the output interface
 transfers one of valid video data and command data and asserts one of the valid data signal and
 the valid command signal to the receiver in response to a request signal received from the
 receiver.
2. (Currently Amended) An input interface for a receiver of video data having, comprising:
 an a first input for receiving data, and
 a second input for receiving a valid data signal associated with the data received by the
 first input and indicating whether the associated received data includes valid video data, and
 a third input for receiving a valid command signal indicating whether the data received
 by the first input includes command data, and wherein the command data includes a memory
 address at the receiver,
 wherein the first, second and third inputs are in parallel;
 wherein the input interface stores command data received by the first input in memory of
 the receiver; and
 wherein the input interface transfers video data received by the first input to the memory
 address specified in the command data in the memory of the receiver.

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3. (Currently Amended) A device for ~~reading~~ sending video data ~~from~~ to a memory in another device, comprising:

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an output interface ~~having an~~ comprising a first output for providing data, and a second output for providing a valid data signal associated with the data output by the first output and indicating whether the associated output data includes is valid video data, and a third output for providing a valid command signal indicating whether the data output by the first output includes command data, and wherein the command data includes a memory address in the memory in the other device, wherein the first, second and third outputs are in parallel, and having an input for receiving a request signal from the other device; and

wherein, in response to the request signal being received from the other device, the output interface transfers one of valid video data and command data to the other device and asserts one of the valid data signal and the valid command signal. in response to a request signal received from the other device; and an input interface having an input for receiving data, and a valid data signal associated with the data indicating whether the associated data is valid video data.

4. (Currently Amended) The device of claim 3, further comprising:

a memory; and

an input interface comprising a first input for receiving data, and a second input for receiving a valid data signal associated with the data received by the first input and indicating whether the received data includes valid video data, and wherein the input of the input interface further receives a third input for receiving a valid command signal indicating whether the data received by the first input includes command data, wherein the command data includes a memory address in the memory of the device, wherein the first, second and third inputs are in parallel, wherein the input interface stores command data received by the first input in memory of the device, and wherein the input interface transfers valid video data received by the first input to the memory address specified in the command data in the memory of the device.

5-8. Cancelled.

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cancel

9. (Currently Amended) A device for receiving video data from another device, comprising:

- a memory for storing the video data;
- an input interface ~~having comprising an~~ a first input for receiving data, and a second input for receiving a valid data signal indicating whether the data received by the first input includes valid video data, and a third input for receiving a valid command signal indicating whether the data received by the first input includes command data, wherein the command data includes a memory address in the device, wherein the first, second and third inputs are in parallel, wherein the input interface stores command data received by the first input at the device, and wherein the input interface stores video data received by the first input from the other device in the memory in the device at the memory address specified in the command data stored at the device; and
- an output interface having an output for providing a request signal, wherein the other device transfers the video data to the device in response to the request signal.

10. Cancelled.

11. (Currently Added) The device of claim 9, further comprising:

- an output interface comprising a first output for providing data, a second output for providing a valid data signal associated with the data output by the first output and indicating whether the output data includes valid video data, a third output for providing a valid command signal indicating whether the data output by the first output includes command data, and wherein the command data includes a memory address in the memory in a third device, wherein the first, second and third outputs are in parallel, and an input for receiving a request signal from the third device; and

- wherein, in response to the request signal being received from the third device, the output interface transfers one of valid video data and command data to the other device and asserts one of the valid data signal and the valid command signal.
